



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

Shunpei YAMAZAKI

Serial No. 09/433,705

Filed: November 4, 1999

For: SEMICONDUCTOR DEVICE

HAVING LDD REGIONS

(As Amended)

Art Unit: 2811

Examiner: S. Loke

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with  
The United States Postal Service with sufficient postage as First  
Class Mail in an envelope addressed to: Commissioner for Patents,  
Washington, D.C. 20231, on 7/19/02

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits an attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. It is respectfully requested that an initialed copy of this form be returned to the undersigned. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

These documents were cited in an Information Disclosure Statement filed June 7, 2002 with abstracts. Full English translations are submitted herewith. The references cited in the Information Disclosure Statement may be closely related to the claimed invention and the Examiner is requested to carefully review these references.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,

Eric J. Robinson  
Reg. No. 38,285

Robinson Intellectual Property Law Office  
PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165  
(571) 434-6789 (Voice)

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